

III Semester B.Sc. (IT) Examination, June/July 2010
COMPUTER ORGANIZATION AND PC MAINTENANCE

Time : 3 Hours

Max. Marks : 75

Instruction : Answer ***all*** questions from Part – A and answer ***any five*** questions from Part – B.

PART – A

1. What is the purpose of IR ? **(10×2+1×5=25)**
2. Why is data bus ?
3. What is meant by direct addressing ?
4. What is Micro instruction ?
5. What is cache memory ?
6. What is synchronous transfer ?
7. What is polling ?
8. What is disk caches ?
9. What is bus mastering ?
10. What is peer-to peer lan ?
11. Explain the following terms :
 - a) MAR
 - b) DMA
 - c) HMA
 - d) HDD
 - e) VLSI

P.T.O.

PART – B

(5×10=50)

1. How does the cpu interact with the main memory ? Explain with necessary block diagram.
 2. Explain the addressing methods.
 3. What are the necessary steps involved in the execution of an instruction ?
 4. With a neat diagram explain the hardwired control.
 5. Explain cache memory and virtual memory.
 6. Describe briefly the internal organization of memory chips.
 7. Explain different standard I/O interfaces.
 8. Explain the term : memory bank, BIOS, HMA, UMA.
-